

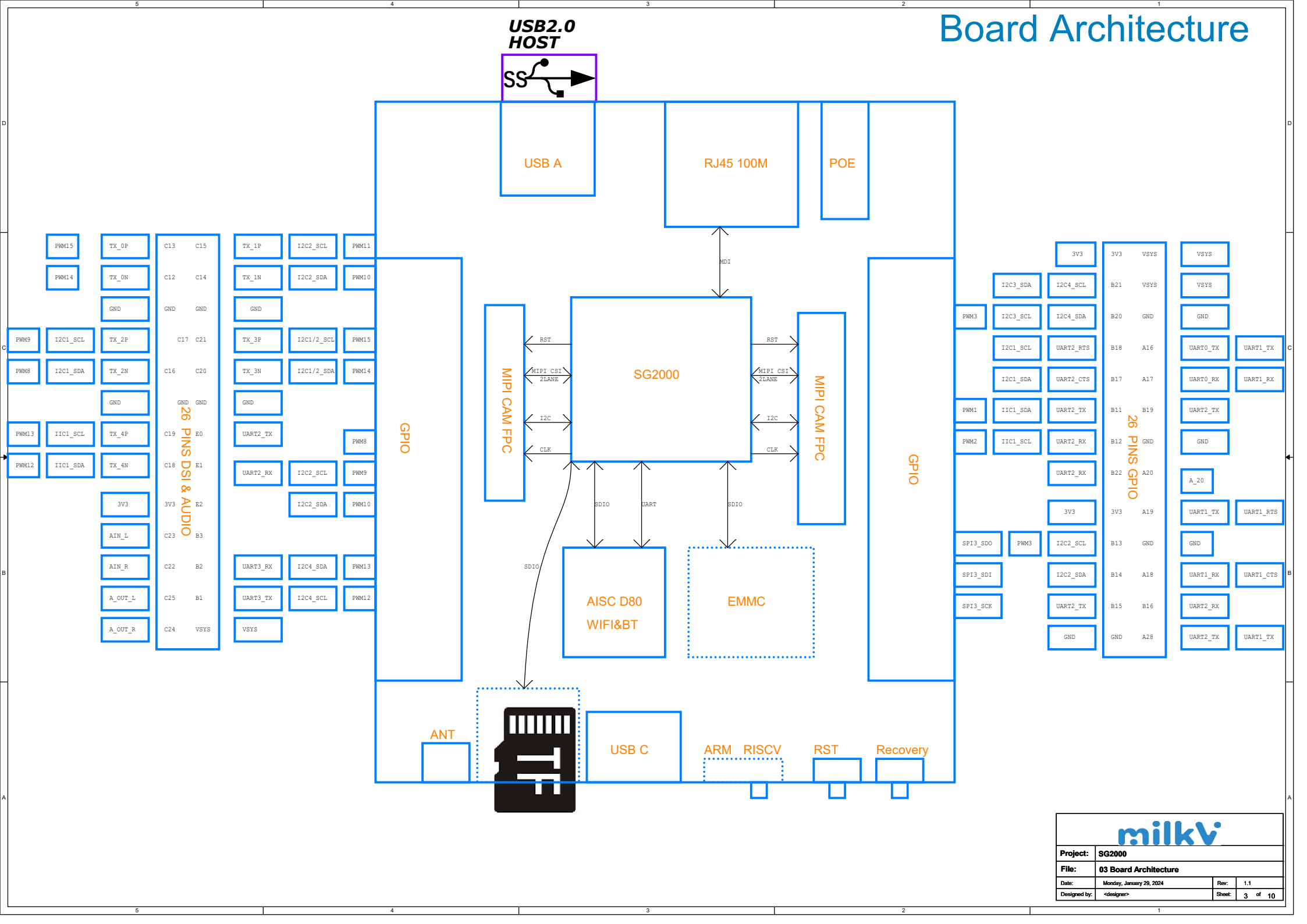
PROJECT COVER

Board Name	DUO S
Board Version	V1.1
Designer	
Reviewed by	

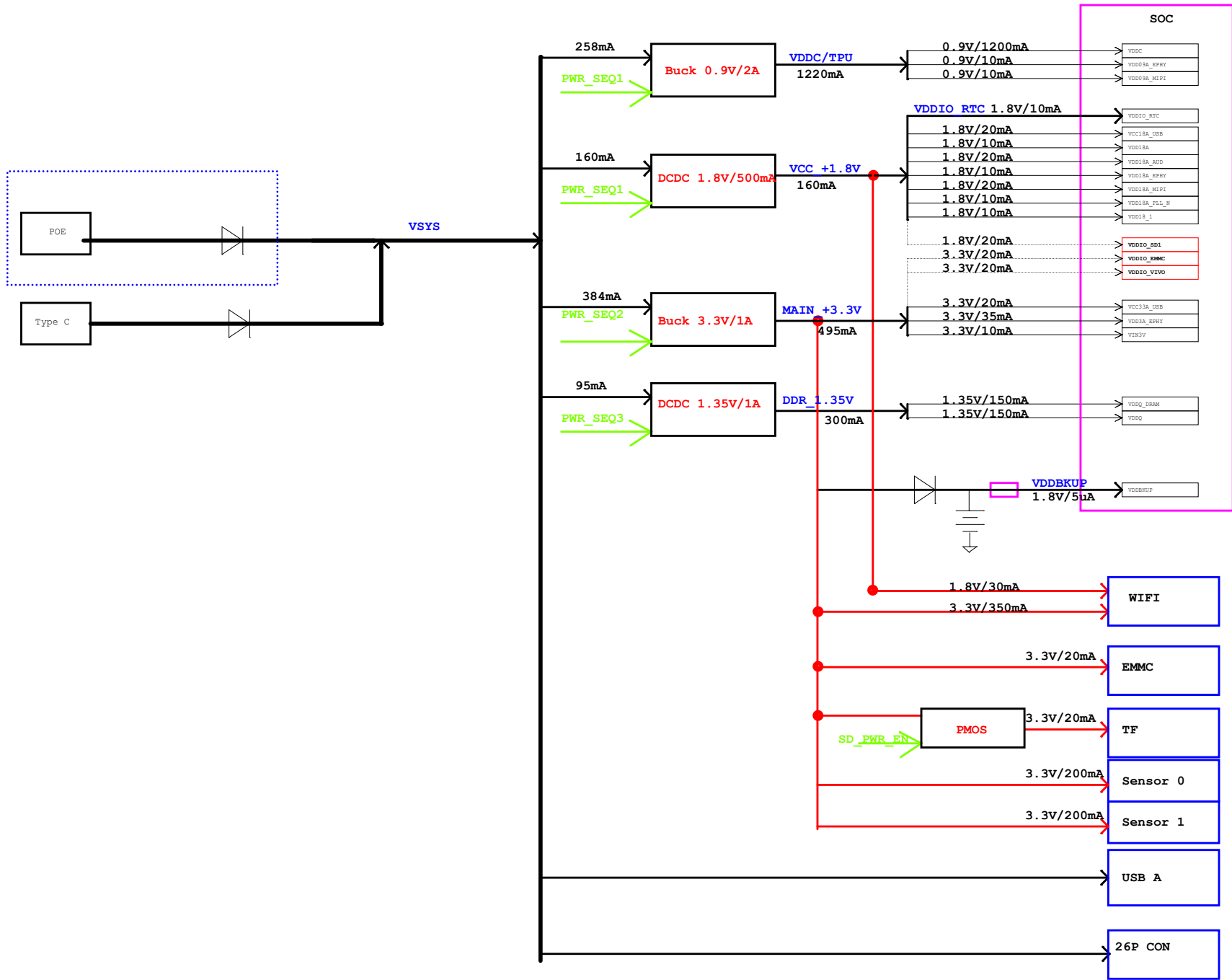
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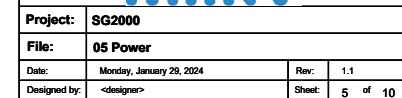
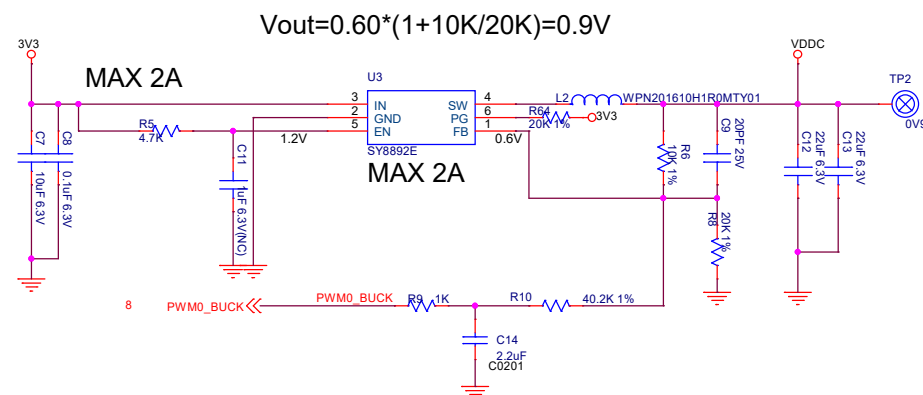
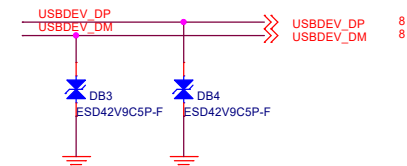
PAGE	TITLE
1	Project Cover
2	Content
3	Board Architecture
4	Power Tree
5	Power
6	SG2000 PART1
7	SG2000 PART2
8	SG2000 PART3
9	SG2000 PART4&EMMC
10	WIFI&BT
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	
21	
22	
23	

Board Architecture



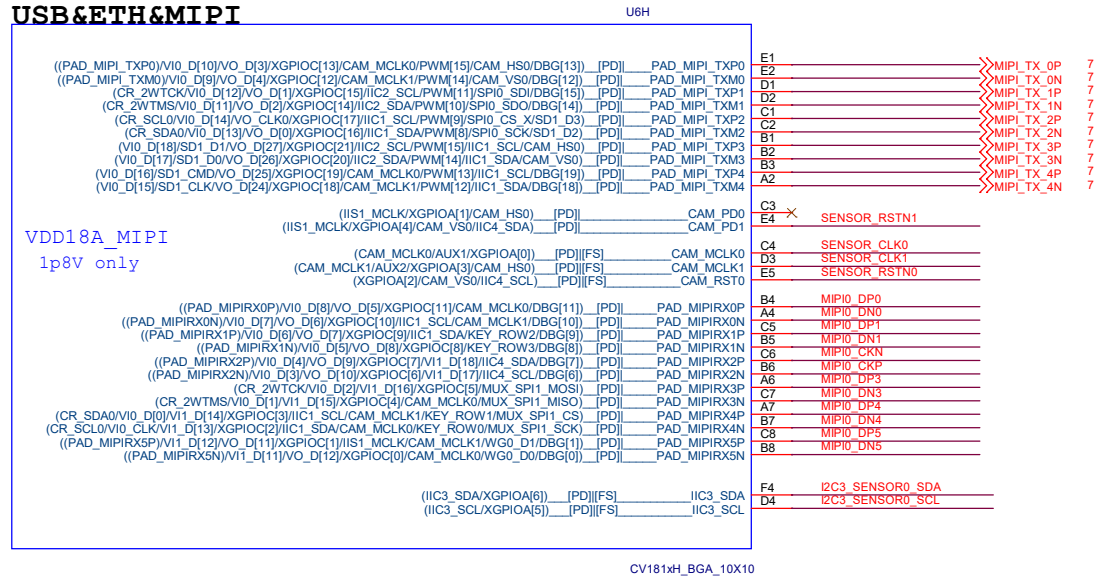
Project:	SG2000		
File:	03 Board Architecture		
Date:	Monday, January 29, 2024	Rev:	1.1
Designed by:	<designer>	Sheet:	3 of 10



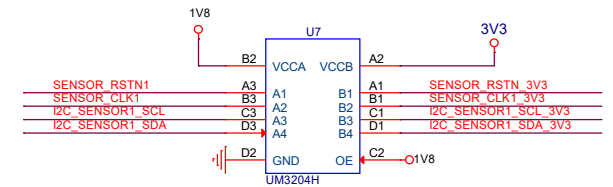
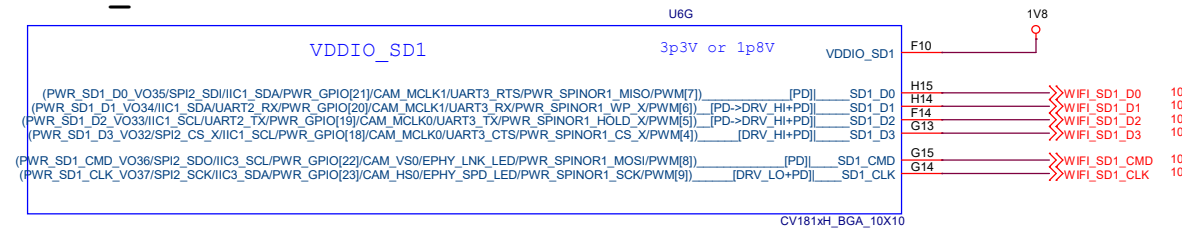


SG2000 PART I

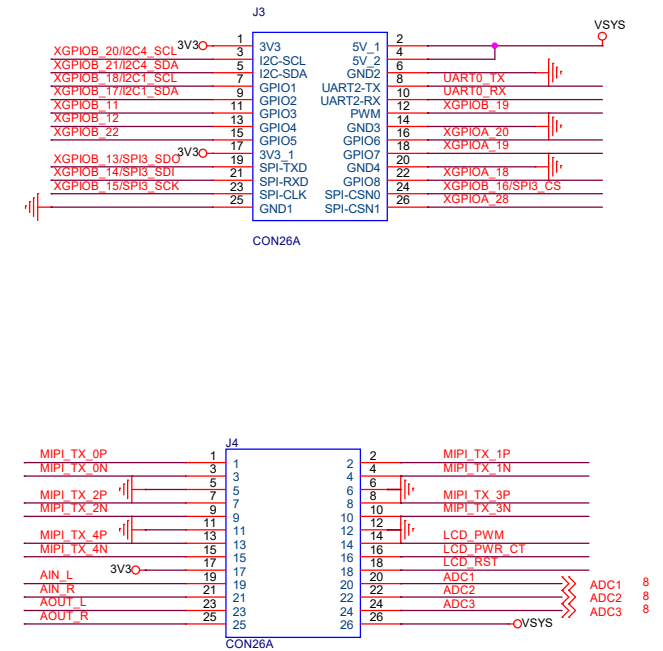
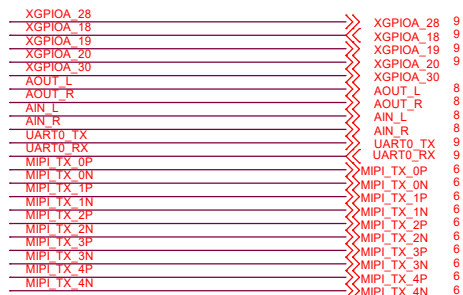
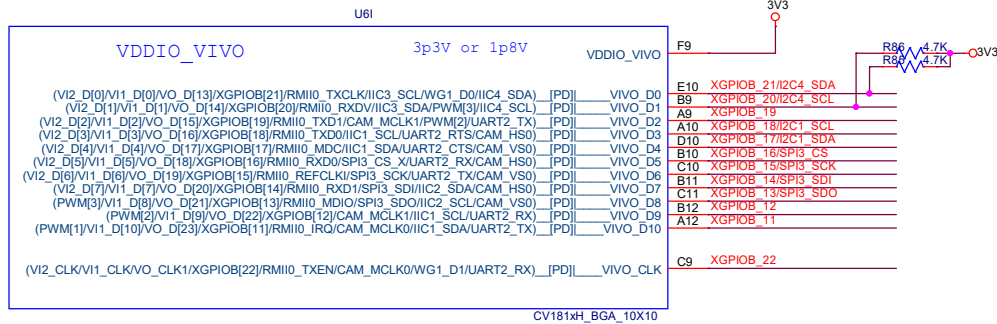
USB&ETH&MIPI



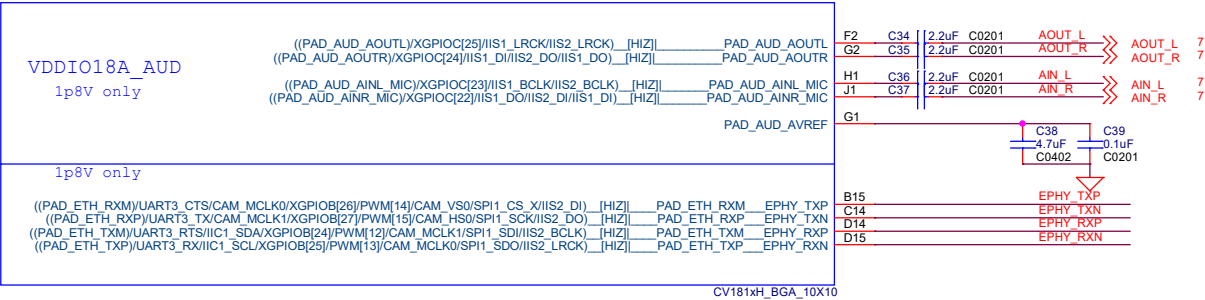
VDDIO_SD1



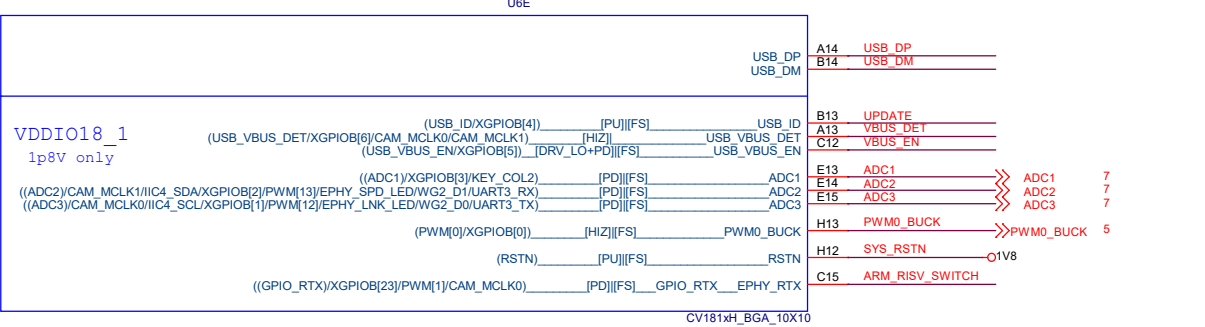
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File:	06 SG2000 PART1
Date:	Monday, January 29, 2024
Designed by:	<designer>
Rev:	1.1
Sheet:	6 of 10



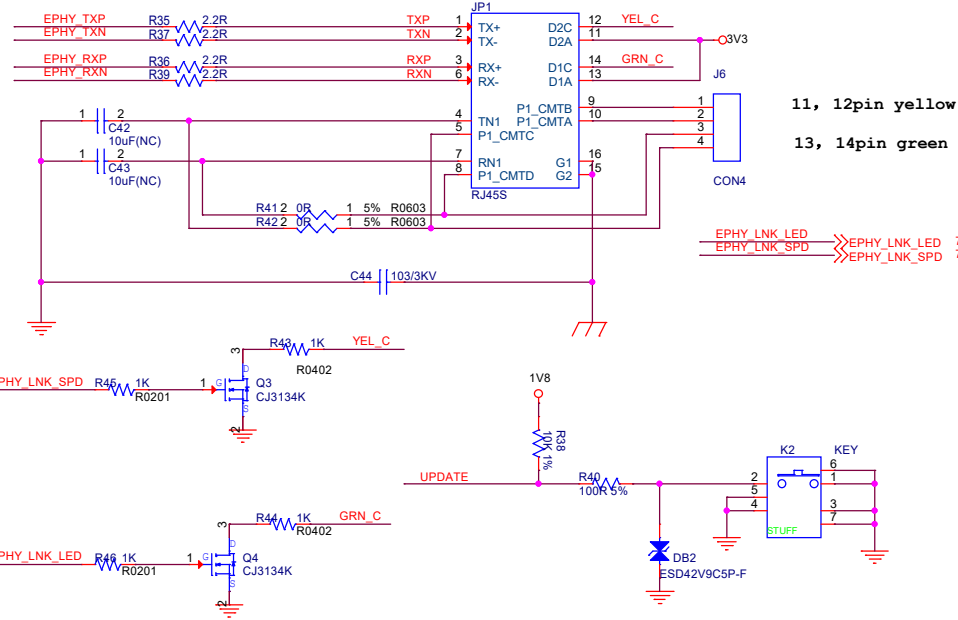
AUDIO&EPHY



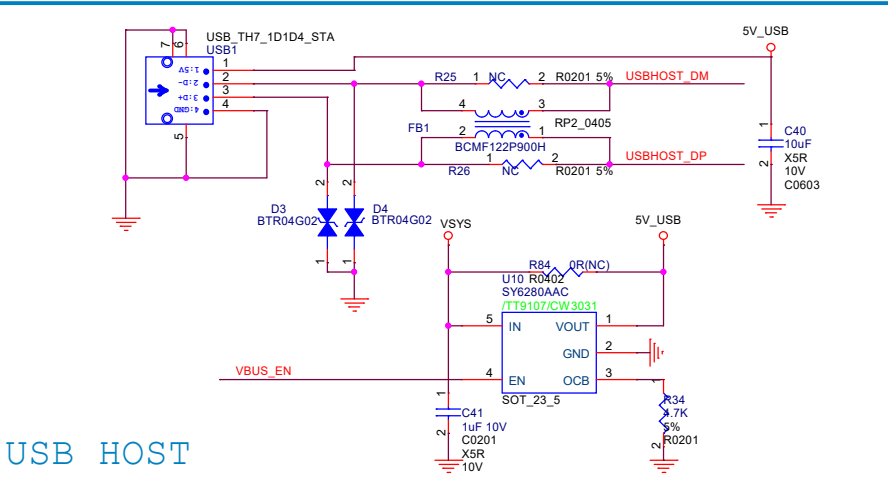
USB&VDDIO18



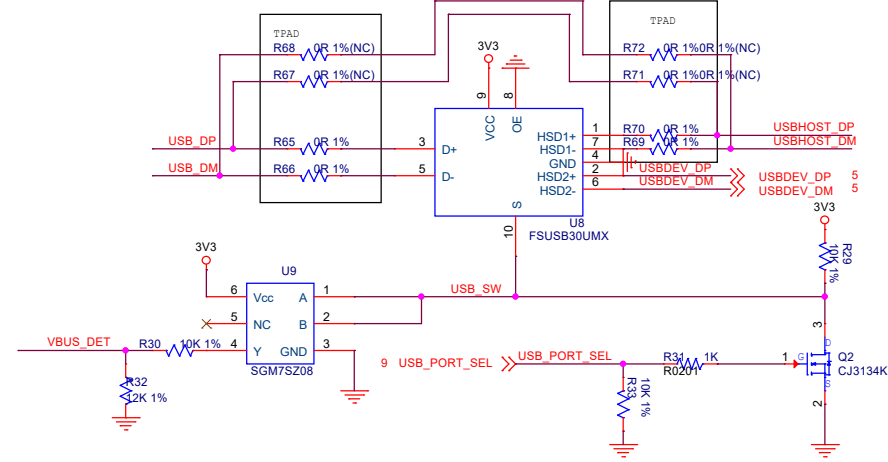
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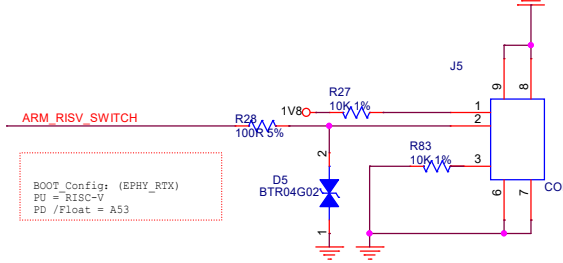
SG2000 PART III



USB SWITCH

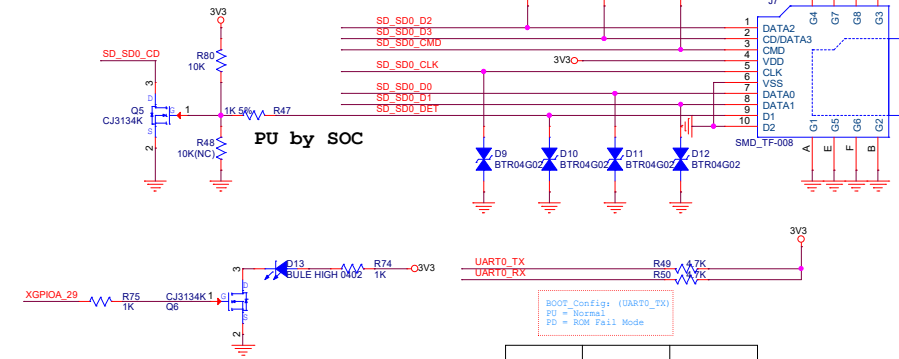


ARM RISC-V SWITCH

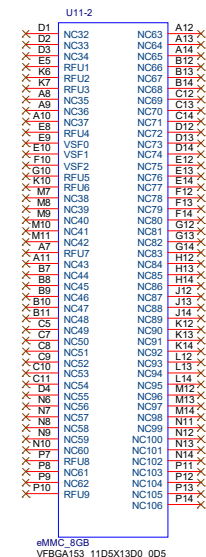
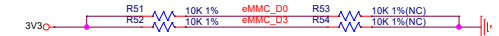


Project: SG2000			
File: 08 SG2000 PART3			
Date: Monday, January 29, 2024	Rev: 1.1		
Designed by: <designer>	Sheet: 8	of 10	

TF_SOCKET



eMMC_D0	eMMC_D3	boot device selection
1	1	eMMC
1	0	Serial NOR
0	0	Serial NAND



eMMC_8GB
VFBGA153_11D5X13D0_0D5

Project:	SG2000		
File:	09 SG2000 PART4&emmc		
Date:	Monday, January 29, 2024	Rev:	1.1
Designed by:	<designer>	Sheet:	9 of 10

